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OFFICIAL

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chambers
Serial No: 09/902,051
Filed: 07/10/01
For: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE
HIGH-PASS TRANSFER FUNCTION

Docket No: TI-30883
Examiner: Davidson, Dan
Art Unit: 2651

#7
11-13-03

RESPONSE

Assistant Commissioner For Patents
Washington, DC 20231

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being
transmitted by facsimile to the U.S. Patent and Trademark
Office at 703-872-9306 on 11-7-03:

Tommy Chambers
Tommy Chambers

Dear Sir:

Responsive to the Office Action mailed September 9, 2003 in connection with the
above identified application, Applicants respectfully submit the following remarks.



To: **Techonology Unit: 2651** **NOV 07 2003** **Total Pages Sent 4**
Facsimile Number: 703-872-9306

From: **W. Daniel Swayze, Jr.**
Texas Instruments Incorporated
Facsimile: 972-917-4418
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant: Chambers

Docket Number: TI-30883

Serial No.: 09/902,051

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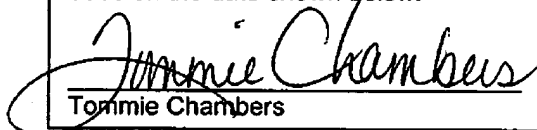
Art Unit: 2651

Examiner: Davidson, Dan

For: **CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE HIGH-PASS TRANSFER FUNCTION**

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Tommie Chambers

November 7, 2003
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NAME OF INVENTOR(S): Chambers	
RECEIPT DATE & SERIAL NO.: Serial No.: 09/902,051 Filing Date: 07/10/01	
TITLE OF INVENTION: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE HIGH-PASS TRANSFER FUNCTION	
TI FILE NO.: TI-30883	DEPOSIT ACCT. NO.: 20-0688
FAXED: 11/07/2003 DUE: 12/09/2003 ATTY/SEC'Y: WDS/tlc	

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